



MS AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2634

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PATENT
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IN THE U. S. PATENT AND TRADEMARK OFFICE

Applicants:	Hitoshi NAOE	Conf.: 9234
Appln. No.:	09/593,945	Art Unit: 2637
Filed:	June 15, 2000	Examiner: G. Munoz
For:	BIT SYNCHRONIZING CIRCUIT	

AMENDMENT UNDER 37 C.F.R. § 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 31, 2005

Sir:

In response to the Office Action dated November 30, 2004, the following amendments and remarks are respectfully submitted in connection with the above-identified application as follows:

Amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper

Remarks/Arguments begin on page 8 of this paper.

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